



Defence Tech Summit

Accelerating Multi-Domain Readiness Through Digital Engineering

Sept 29, 2026 | 09:00 AM | USI, Delhi



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As defence systems become increasingly complex and operations span multiple domains, armed forces, acquisition organizations, and defence R&D establishments are seeking new approaches to accelerate capability delivery while reducing program risk and achieving operational advantage in a rapidly evolving battlespaces. This forum brings together senior leaders from the Armed Forces, HQ IDS, DRDO, DPSUs, academia, and industry to explore how Model-Based Acquisition (MBAcq), digital engineering, mission engineering, AI, digital twins, and simulation-driven approaches can transform the journey from requirements to operational readiness.

Agenda

Session 1 - Plenary		
0900 - 0930	Registration	
0940 - 0945	CDS Arrival	
0945 - 0955	Welcome Address – V Adm SJ Singh, DG USI	
0955 - 1005	Setting the Context – Maj Gen Pawan Anand, Director CETANB	
1005 - 1025	Key Note Address (Multi Domain Operations and Related Technologies) Gen Raja Subramani, CDS	
1030 - 1045	Tea Break (CDS departs 1035h)	
1045 - 1055	Spl Address -1. Fast-Tracking Defence Capability Development Through Model-Based Acquisition	
1055 - 1105	Spl Address-2. Accelerating Defence transformation through digital engineering and certification by Analysis. Prem Andrade, Distinguished Engineer (M/s Ansys)	
1105 - 1115	Spl Address-3 Operationalising Multi-Domain Capability in the Indian Context Lt Gen Vineet Gaur, DG Capability Development	
1115 - 1120	Release of White Paper on Model Based Acquisition	
	Session 2 - Breakout (RTD Format)	
1130 - 1300	Track - 1	Track - 2
Theme	Mission Systems, Spectrum Operations & Autonomous Capabilities	Engineering Assurance, Certification & Digital Thread
1300 - 1400	Networking Lunch and solution demonstrations	
1400 - 1500	Track - 1	Track - 2
Theme	Evaluating Electromagnetic Spectrum Operations in the Digital Battlespace	Engineering Trustworthy Defence Systems Through MBSE and Safety Analysis
	Reducing Program Risk Through Virtual Validation of Physical Systems	Connecting Requirements to Readiness Through the Digital Thread
	Session 3 - Valedictory	
1500 - 1600	Development & Adoption Approaches for AI & Digital Twin. Dr Jaijit Bhattacharya President CDEP Maj Gen Pawan Anand, AVSM, PhD (Retd) - Moderator Release of CDEP Report on Digital Sovereignty	
1600 - 1630	Closing Notes & Vote of Thanks	
1630	Tea & Dispersal	